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**ABU SAYED MD. MOSTAFIZUR RAHAMAN**  
**ASSISTANT PROFESSOR**  
**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**  
**JAHANGIRNAGAR UNIVERSITY, SAVAR, DHAKA, BANGLADESH**  
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<b>Career objective</b>	Become a versatile person who is capable of merging innovative ideas, technology, knowledge and experience for positive contribution towards the system development in the rapidly changing scenario of Information technology and to be a good teacher.
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<b>Books:</b>	<ol style="list-style-type: none"><li>1. Laboratory Materials for Digital System Design, 2011.</li><li>2. Laboratory book on Electronics and Computer Engineers. 2012 (Under processing).</li></ol>
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<b>Research Publications</b>	<ol style="list-style-type: none"><li>[1]. "Parallel Commutation of Sparse Linear Systems on many core Processor", Proc. 14<sup>th</sup> Int. Conf. on Computer and Information Technology, held in December 2011.</li><li>[2]. "Compensation of Fading Affect of Two-hop Wireless Link using Adaptive Equalization", IACSIT International Journal of Engineering and Technology, Vol.3, No. 6, December 2011, Singapore.</li><li>[3]. "Performance Evaluation of Two-hop Wireless Link under Rayleigh and Nakagami-m Fading Channel for 8-PSK and 16-QAM", IACSIT International Journal of Engineering and Technology, Vol. 3, No.5, October 2011, Singapore</li><li>[4]. "Reliable Modulation method in PLC Communication for Home Network", Jahangirnagar University Journal of Science, v-28, December, 2005</li><li>[5]. "Genetic Programming: Novel Network Attacks Detection", Proc. 8<sup>th</sup> Int. Conf. on Computer and Information Technology, pp. 722-727, 2005.</li><li>[6]. "Image Understanding for Human-Robot Symbiosis: Eye Tracking and Scene Interpretation", Proc. 8<sup>th</sup> Int. Conf. on Computer and IT, pp. 261-265, 2005.</li><li>[7]. "Lower Overhead Authentication Stream using Erasure Codes", Proc. 8<sup>th</sup> Int. Conf. on Computer and Information Technology, pp. 1106-1111, 2005.</li></ol>
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- [8]. "Justification of High Spectral Efficiency of VPSK Digital Modulation", Journal of Mathematics and Mathematical Sciences, v-19, p. 35-42, 2004.
- [9]. "Bezier Curve as an Efficient Off-Line Path Planner", Journal of Mathematics and Mathematical Sciences, v-19, p. 63-72, 2004.
- [10]. "Simulation of Power Line Carrier Communication over low voltage grid", Proc. 6<sup>th</sup> Int. Conf. on Computer and Information Technology, 2003

#### Experience

- (1) **October'04- Present**  
**ASSISTANT PROFESSOR**  
 Dept. of Computer Science & Engineering  
 Jahangirnagar University, Savar, Dhaka, Bangladesh
- (2) **Industrial Internship**  
**ROBERT BOSCH** GmbH (ESV4/DA) , Leonberg, Stuttgart, Germany  
 From October'08 to March'09, Using C++ and Qt  
 Task: Conceptual Graph Implementation for Vehicle Environment System.
- (3) **Student Assistantship :**
- a. June'07 to September'07  
 Institute of Computer Architecture and Computer Engineering  
 University of Stuttgart, Germany.
  - b. October'07 to March 2008  
 Using C#, Visual Studio 2005 and XML  
 HLRS(High Performance Computing Center)  
 University of Stuttgart, Germany
- (4) **July'03 -September '04:**
- Lecturer**  
 Daffodil Institute of IT  
 Under London Metropolitan University  
 Banani, Dhaka, Bangladesh.

**Master Thesis** **Matrix Based Parallel Multigrid Solver in Reconfigurable Hardware.** Under the supervision of Professor Dr. Simon, Department of Parallel System, University of Stuttgart, Germany.  
**Language used : C/C++**  
**Benchmark: MATLAB and CUDA.**

**Undergraduate Research Project** "Simulation of Power Line Carrier Communication over low voltage grid", using **Matlab/Simulink** and Electronic Workbench. Under the supervision of Professor Dr. Akram Hossain, Dept. of Computer Science and Engineering, Jahangirnagar University, Savar, Dhaka, Bangladesh.

**Research Interest** Wireless communications, VLSI design, Embedded Systems, Computer Architecture, Parallel and Reconfigurable Computing.

**Academic Record**

October'09	<b>M.Sc in Information Technology</b> <b>Major: Embedded System Engineering</b> University of Stuttgart, Germany.
2003	<b>4-years B.Sc(Hons.)</b> Dept. of Electronics and Computer Science Jahangirnagar University, Dhaka, Bangladesh
1996	<b>Higher Secondary School Certificate (H.S.C)</b> Govt. Science College, Dhaka, Bangladesh
1994	<b>Secondary School Certificate (S.S.C)</b> Dinajpur Zilla School, Dinajpur, Bangladesh

**Taught Courses**

- VLSI design (B.Sc.)
- Computer Architecture(B.Sc.)
- Compiler Design (B.Sc.)
- Digital System Design(B.Sc.)
- Digital Signal processing(B.Sc.)
- Mobile Communication Systems(M.Sc.)
- Network Management (M.Sc.)

**PERSONAL DETAILS**

Name : Abu Sayed Md. Mostafizur Rahaman  
Father's Name : Md. Nayeb Ali  
Date & Place of birth : 12 January 1980, Bangladsh  
Sex & Marital Status : Male, Married

**Present Address:**  
D-25, Jahangirnagar University Residential Area  
Jahangirnagar University,  
Savar, Dhaka, Bangladesh

**Permanent Address:**  
Village: Boro Chandipur (Master Para)  
Post: Habra  
Thana: Parbatipur  
Zilla: Dinajpur

*Sincerely,*



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**Abu Sayed Md. Mostafizur Rahaman**