

Dr S. Kishore Reddy

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Objective

A challenging and rewarding position in the field of VLSI design, Design Methodology and educational development, and work for an organization which brings my skills to an optimum level thereby the organizational goals are achieved.

Profile

- Total 11 Years of experience
- Trained by National Institute of Technical Teacher Training and Research (NITTTR), Chennai.
- Trained on Cadence Back End Tools.
- Life Time member of ISTE
- 23 Research papers published on international journals
- Present the paper on 15 international conference
- Present the paper on Two national conference
- Design the blog for academic and research work

Area of Interest

VLSI Design

Subjects handled

M.Tech (VLSI DESIGN):

Electronic Design Automation Tools
Digital System Design.
Algorithm for VLSI design automation
VLSI technology
Embedded & Real time operating system
DSP processors and Architectures

B.Tech (ECE):

VLSI technology
Satellite communications
Digital signal processing
Digital IC Applications
Electronic Devices and Circuits
Computer Organization
Embedded Systems
Television Engineering
Digital Communication
DSP processors and Architectures

Professional Experience

Worked as Asst. Prof in **CVR engineering college** from June 2002 to August 2004 date.

Worked as Asst. Prof in **Padmasri Dr. B.V. Raju Institute of Technology Narsapur, Medak (Dt.)** from September 2004 to June 2010 date.

Worked as Asst. Prof in **Sri Kavitha Engineering College Karepalli, Khammam (Dt.)** from July 2010 to September 2010 date.

Worked as **Associate Professor** in **Sarada Institute of Technology & Science, Khammam** from October 2010 to May 2012 date.

Working as **Associate Professor** in **Swarna Bharathi College of Engineering, Khammam** from June 2012 to Nov 2012.

Working as **Associate Professor** in **SR Engineering College Warangal Andhra Pradesh India** from November 2012 to till the date.

Skill Set

- **HDL Languages:** VHDL, Verilog, perl.
- **EDA TOOLS**
Simulation tools : ModelSim III v 6.2g., Xilinx ISE simulator, Active HDL , Matlab, Code composed studio
Synthesizing tools: Xilinx 9.2i, Quartus II 6.2, Leonardo Spectrum
Cadence Tools : IUS, RTL Compiler, SOC Encounter, Virtuoso, Assura
Layout tools : Microwind 3.0, Tanner Pro 10.20.

Academic Qualification

- **Doctor of philosophy (PHD)** in ECE from CMJ University.
- **Master of Technology in VLSI Design** from CVR College of Engineering , University of JNTU, A.P , **(80%)**
- **Bachelor of Engineering (E.C. E)** from SRR Engineering college, University of JNTU, A.P. **(69%)**
- **Board of Intermediate Education** from shanti Jr college khammam. **(83.5%)**
- **SSC** from Sathupalli Public School. **(70%)**

Workshops

- Participated in 2-days short term intensive course on **CADANCE TOOLS** conducted by a **JNTUH**
- **Conducted a national level Technical Feast on SARADA INSTITUTE OF TECHNOLOGY & SCIENCE**
- Participated in 2-weeks workshop on **IMAGE PROCESSING** conducted by **BVRIT**.
- Participated in 3-days short term intensive course on **IMAGE PROCESSING & APPLICATIONS** conducted by a **JNTUH**.
- Participated in 2-weeks workshop on **Back-End VLSI Design** using Cadence tools conducted by **CVR Engg College**.
- Participated in 2-days short term intensive course on **VLSI Design** at **BVRIT**

- Participated in 2-days workshop on **Teacher Training** conducted by NITTR at BVRIT.
- Participated in 2-days workshop on **Mat lab** conducted by **BVRIT**.

PG Projects (M.TECH) Undertaken : 8

UG Projects Undertaken : 16

Personal Details

- **Date Of Birth** : 05/09/1984
- **Permanent Address:** S.kishoreddy
S/o venkatareddy
Narayanapuram (v&p)
Sathupally (MD)
Khammam (DT)-507303

Present address

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