

KONGUVEL E.

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**Statement of Interests**

- To strive for academic excellence and impart quality education with emphasis on Electronics and Communication Engineering.
- To inculcate the habit of executing effective implementation and rectification of Electronics and Communication Engineering problems in the minds of all concerned, which they can apply in their professional life in future.

Professional Experience

Organization	Designation	From	To	Duration
Anna University – MIT Campus	Teaching Fellow	25 Nov 2015	Till Date	
Dhanalakshmi Srinivasan College of Engineering, Coimbatore	Assistant Professor	26 June 2013	23 Nov 2015	2 Years 5 Months
Shivalaya Alloys and Steels Private Limited, Coimbatore	Electrical Engineer	2 June 2010	13 May 2011	1 Year

*Presently Working

Educational Qualifications

Degree/ Course	Specialization	Year	Name of the Institution/ University	Marks	Class
Ph.D.	Digital VLSI Design	2014 – Pursuing	Anna University – MIT Campus, Chennai.	Course Work Completed	
M.E.	Communication and Network Engineering	2011 – 2013	Anna University – MIT Campus, Chennai.	78%	First
B.E.	Electronics and Communication Engineering	2006 – 2010	Karpagam College of Engineering, Coimbatore. Anna University, Chennai.	69%	First
H.S.C.	Maths – Biology	2005 – 2006	SRV Boys Hr. Sec. School, Rasipuram / State Board	90%	First
S.S.L.C.	Maths – Science	2003 – 2004	Kongu Matric. Hr. Sec. School, P.Velur / Matric	93%	First

Research Interests

Digital System Design, VLSI for Signal Processing and Communication.

Tentative Title of Ph.D. Research Work

“VLSI Implementation of a Reconfigurable FFT/IFFT Processor for MIMO-OFDM based Applications”

Projects

1. VLSI Implementation of Multi-User OFDM to suppress phase noise and carrier frequency offset.
2. FPGA Implementation of Advanced Encryption Standard (AES) encryption and decryption algorithm.

Computer Proficiency

VLSI Tools : Altera Quartus II, Altera ModelSim, Xilinx ISE, Cadence design suite.

Networking Tools : Cisco Packet Tracer.

DSP Tools : MATLAB.

Publications

Total	:	18		
Journals	:	National : Nil	International	: 10
Conferences	:	National : 5	International	: 3

Journal Publications:

1. **E.Konguvel** & M.Kannan, “A Study on Sequential and Pipelined FFT/IFFT processors for Next Generation Communication Systems”, *Current Science*. (Communicated)
2. Simi Wilson, M.Thangamani & **E.Konguvel**, “Detection of Breast Tumour and Speckle Noise Removal using Bilateral Filter and Bivariate Shrinkage”, *International Journal of Computer Applications*, 0975-8887, Vol. 116, No. 3, pp. 42-45, April 2015.
3. G.Karpagam, **E.Konguvel** & M.Thangamani, “A Low Power VLSI Implementation of STTRAM based TCAM for High Speed Switching Circuits”, *International Journal of Computer Applications*, 0975-8887, Vol. 115, No. 7, pp. 38-43, April 2015.
4. J.Eric Clapten, **E.Konguvel** & M.Thangamani, “VLSI Implementation of Low Power Area Efficient Fast Carry Select Adder”, *International Journal of Computer Applications*, 0975-8887, Vol. 115, No. 6, pp. 5-8, April 2015.
5. R.J.Egusha, B.Mathivanan & **E.Konguvel**, “A Distributed LeDir algorithm for WSA Networks”, *IRACST – Engineering Science and Technology: An International Journal*, 2250-3498, Vol. 4, No. 3, pp. 88-91, June 2014..
6. Colins Antony & **E.Konguvel**, “Vehicle Detection based on Feature Extraction and SVM classification”, *IOSR Journal of Computer Engineering*, 2278-0661, Vol. 1, Special Issue, pp. 42-47, May 2014.
7. Y.Faizal & **E.Konguvel**, “An Approach for Unconstrained Face Recognition Algorithm using Computer Vision Method”, *IOSR Journal of Computer Engineering*, 2278-0661, Vol. 1, Special Issue, pp. 01-06, May 2014.
8. Colins Antony & **E.Konguvel**, “Vehicle Detection System using SVM classification and Haar Filter”, *CiiT International Journal of Fuzzy Systems*, 0974-9608, Vol. 6, No. 2, pp. 33-36, April 2014.

9. N.V.Shimna, **E.Konguvel** & J.Raja, "A Modified CORDIC processor for Specific Angle Rotation based Applications", *IOSR Journal of VLSI and Signal Processing*, 2319-4200, Vol. 4, No. 2, pp. 29-37, March 2014.
10. A.Amjadha, **E.Konguvel** & J.Raja, "Design of Multipath Delay Commutator Architecture based FFT Processor for 4th Generation Systems", *International Journal of Computer Applications*, 0975-8887, Vol. 89, No. 12, pp. 23-28, March 2014.
11. **E.Konguvel**, J.Raja & M.Kannan, "A low power VLSI Implementation of 2X2 MIMO OFDM transceiver with ICI-SC scheme", *International Journal of Computer Applications*, 0975-8887, Vol. 77, No. 5, pp. 9-15, September 2013.

Conference Publications:

1. N.V.Shimna & **E.Konguvel**, "VLSI Implementation of Modified CORDIC processor for Vector Rotations", *National Conference on Advancements in Computing Methodologies (ACM'14)*, 27 March 2014.
2. N.V.Shimna & **E.Konguvel**, "A Modified CORDIC processor for Angle Rotations", *National Conference on Recent Trends in Electronics and Communication (RTEC'14)*, 21 March 2014.
3. A.Amjadha & **E.Konguvel**, "Design of Multipath Delay Commutator Architecture based FFT/IFFT Processor", Proc. of *National Conference on Signal Processing, Communication and Nanotechnology (NCSCN'14)*, pp. 15-20, 20 March 2014.
4. Colins Antony & **E.Konguvel**, "Vehicle Detection based on Feature Extraction and SVM classification", *International Conference on Recent Innovations in Engineering (ICRIE'14)*, 13 March 2014.
5. Y.Faizal & **E.Konguvel**, "Colour Space model with Chan-Vese Algorithm for Fire Detection in Videos", *International Conference on Recent Innovations in Engineering (ICRIE'14)*, 13 March 2014.
6. Colins Antony & **E.Konguvel**, "Vehicle Detection system using SVM classification and Haar Transform", Proc. of *Third National Conference on Emerging Trends in Computer Communication and Informatics (ETCCI'14)*, 9780989425476, pp. 26, 03 March 2014.
7. Y.Faizal & **E.Konguvel**, "An approach for Unconstrained Face Recognition Algorithm using Viola – Jones method", Proc. of *Third National Conference on Emerging Trends in Computer Communication and Informatics (ETCCI'14)*, 9780989425476, pp. 16, 03 March 2014.
8. **E.Konguvel** & M.Kannan, "VLSI Implementation of Multi-User OFDM to suppress Phase Noise and Carrier Frequency Offset", Proc. of *International Conference on Electrical Engineering and Computer Science (ICEECS'13)*, pp. 152-158, 17 March 2013.

Participations

S.No.	Title of the Workshops/ Seminars/FDPs/Webinars	Organizer	Date	Duration
1.	MATLAB for Engineering Applications	VIT University, Chennai	29 August 2015	1 Day
2.	Audio Slides for Research	Elsevier Journals	8 June 2015	1 Day
3.	Research Methodology	Anna University, Chennai	10 December 2014	1 Day
4.	Elsevier Publishing Connect	Anna University, Chennai	23 September 2014	1 Day

5.	Effective Teaching and Research	PSG College of Technology, Coimbatore	23 November 2013	1 Day
6.	Fundamentals of Soft Computing Techniques	Akshaya College of Engineering and Technology, Coimbatore	08 November 2013	2 Days
7.	Infuse Life into Research	Kongu Engineering College, Erode	21 September 2013	1 Day
8.	Challenges and Opportunities in ESD	VIT University, Vellore	03 August 2013	1 Day
9.	Embedded Software Development Tools	Kongu Engineering College, Erode	25 July 2013	2 Days
10.	FDP on Soft skills	DSCE, Coimbatore	27 June 2013	2 Days
11.	Electrical Engineering and Computer Science	IIMT, Bhubaneswar	17 March 2013	1 Day
12.	Image processing framework using FPGA	Anna University, Chennai	15 October 2012	2 Days
13.	Analog IC Design	Anna University, Chennai	14 September 2012	2 Days

Subjects handled

S.No.	Course Title	Class	No. of times handled
1.	Embedded Systems	IV – EEE	1
2.	Advanced Digital Signal Processing	I – ME (AE)	1
3.	VLSI Design	III – ECE	1
4.	VLSI for Wireless Communication	I – ME (CS), II – ME (AE)	2
5.	Digital Signal Processing	III – ECE	2
6.	Electronic Devices	I – ECE	1
7.	Multicore Architectures	I – ME (AE)	1
8.	Digital Signal Processing	III – IT	1
9.	Digital Signal Processing	IV – CSE	1
10.	Microprocessors and Microcontrollers	III – EEE	1

Membership in Professional bodies

S.No.	Society/Association	Status	Member ID
1.	International Association of Engineers (IAENG)	Lifetime	131852
2.	International Association of Computer Science and Information Technology (IACSIT)	Lifetime	80346971

3.	Universal Association of Computer and Electronics Engineers	(UACEE)	3 Years	AM1004573
4.	The Science and Engineering Institute	(SCIEI)	Lifetime	20131023005

Accomplishments

1. Received **Letter of Appreciation** from the management of Dhanalakshmi Srinivasan College of Engineering for achieving 100% result in “*VLSI for Wireless Communication*” in the University Examinations held during January 2015.
2. Received **Letter of Appreciation** from the management of Dhanalakshmi Srinivasan College of Engineering for achieving 100% result in “*Advanced Digital Signal Processing*” in the University Examinations held during January 2014.
3. Delivered a **Lecture** on “*Verilog Programming Concepts*” to the students of Dhanalakshmi Srinivasan College of Engineering, Coimbatore, on 11 September 2013.

Responsible positions held in the Department/Institution

1. Anna University Affiliation Co-ordinator for faculty data sheets (Annexure – I & Annexure – II).
2. Anna University Affiliation Co-ordinator for Laboratory Requirements (R – 2008/2013).
3. Class Advisor.
4. Co – Editor, Volume – IV, “PATALI”, a biannual DSCE magazine.

Personal Profile

Name : E.Konguvel
 Gender : Male
 Date of Birth / Age : 08 Nov 1988 / 26
 Father/Mother : K.Elango/R.Shanthi
 Mother tongue : Tamil
 Languages known : English, Tamil
 Marital Status : Single
 Nationality : Indian
 Religion : Hindu
 Community/Caste : BC/Kongu Vellalar

References

1. **Dr. M.Kannan**,
Associate Professor and Head,
Department of Electronics Engineering,
Anna University – MIT Campus,
Chennai – 600044.
044-22516083, mkannan@annauniv.edu.
2. **Dr. J.Raja**,
Professor,
Department of ECE,
Adhiparasakthi Engineering College,
Melmaruvathur – 603319.
+91 9894657971, raja.stj@gmail.com.

Declaration

I hereby declare that all the information furnished above is true to the best of my knowledge and belief.

Place : Rasipuram – 636142.
 Date : 30 Nov 2015


[KONGUVEL E.]